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Bib Data Sheet

CONFIRMATION NO. 3242

<b>SERIAL NUMBER</b> 10/658,195	<b>FILING OR 371(c) DATE</b> 09/10/2003 <b>RULE</b>	<b>CLASS</b> 716	<b>GROUP ART UNIT</b> 2825	<b>ATTORNEY DOCKET NO.</b> 56937-089
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## APPLICANTS

Kenichiro Uda, Osaka, JAPAN;

\*\* CONTINUING DATA *NONE YLR* \*\*\*\*\*

\*\* FOREIGN APPLICATIONS *YES YLR* \*\*\*\*\*  
JAPAN P2002-271362 09/18/2002

IF REQUIRED, FOREIGN FILING LICENSE  
GRANTED \*\* 12/12/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>Helen Kossoshibe YLR</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 5	TOTAL CLAIMS <i>12</i> 13	INDEPENDENT CLAIMS 1
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## TITLE

Structure of power supply path utilized in design of integrated circuit

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